

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Ken UCHIDA, et al.

SERIAL NO: NEW APPLICATION

FILED: Herewith

FOR: LOGIC APPARATUS AND LOGIC CIRCUIT

GAU:

EXAMINER:

1c872 U.S. PTO  
09/990362  
11/23/01

INFORMATION DISCLOSURE/RELATED CASE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☒ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

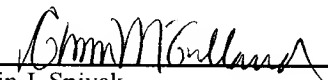
- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

  
Marvin J. Spivak  
Registration No. 24,913

C. Irvin McClelland  
Registration Number 21,124



22850

Tel. (703) 413-3000  
Fax. (703) 413-2220  
(OSMMN 10/98)

**LIST OF RELATED CASES**

<u>Docket Number</u>	<u>Serial or Patent No.</u>	<u>Filing or Issue Date</u>	<u>Status or Patentee</u>
0039-6447-2 SRD	6,060,748	05/09/00	UCHIDA et al.

GJM/sdj

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page 1 of 1

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09/990362  
11/23/01

DOCKET NO.: 216455US2SRD

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Ken UCHIDA, et al.

SERIAL NO.: New Application

FILED: Herewith

FOR: LOGIC APPARATUS AND LOGIC CIRCUIT

**STATEMENT OF RELEVANCY**

**Reference AW on Form PTO-1449:**

This reference describes the standard structure and operation scheme of the FLASH Memories.

**Reference AX on Form PTO-1449:**

This reference describes the concept of "Reconfigurable Computing".

Form PTO 1449  
(Modified)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.  
216455US2SRDSERIAL NO.  
NEW APPLICATION

## LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Ken UCHIDA, et al.

FILING DATE

Herewith

GROUP

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	6,060,748	5/9/00	Ken UCHIDA, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	Hitoshi KUME, et al., "Flash Memory Technology", Central Research Laboratory, Hitachi Ltd., November 1996, pgs. 1114-1124
	AX	T. SUEYOSHI, "Reconfigurable Computing", IPSJ MAGAZINE, Vol. 40, No. 8, August 1999, pgs. 777-782
	AY	
	AZ	

Examiner

Date Considered

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Jc872 U.S. P. 28  
09/990362

11/23/01